

**IN THE CLAIMS:**

Claim 1 is amended herein. All of the pending claims are presented below. This listing of claims will replace all prior versions and listings of claims in the application. Please enter these claims as previously amended.

**Listing of Claims:**

1. (Currently amended) An intermediate structure of a ~~post-probe tested~~ post-circuit repaired semiconductor device comprising:  
at least one open fuse structure on the intermediate structure of the ~~post-probe tested~~ post-circuit repaired semiconductor device; and  
a metal feature on a first metal structure of the intermediate structure of the ~~post-probe tested~~ post-circuit repaired semiconductor device, wherein a metal of the metal feature is present on the first metal structure and is not present on the at least one open fuse structure.
2. (Previously Presented) The intermediate structure of claim 1, wherein the metal feature comprises an electrolessly plated metal feature.
3. (Previously Presented) The intermediate structure of claim 1, wherein the metal feature is a metal layer, an interconnect cap, a redistribution layer, or a bond pad.
4. (Previously Presented) The intermediate structure of claim 1, wherein the metal feature is a metal layer.
5. (Previously Presented) The intermediate structure of claim 1, wherein the metal feature comprises a nickel, palladium, gold, tin, silver, or copper feature.
6. (Previously Presented) The intermediate structure of claim 1, wherein the metal feature comprises a nickel feature.

7. (Previously Presented) The intermediate structure of claim 1, wherein the first metal structure comprises at least one bond pad.

8. (Previously Presented) The intermediate structure of claim 1, wherein the intermediate structure is an intermediate structure of an SRAM or FLASH memory chip.

Claims 9-18 (Canceled)